## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,872,964 B2 Page 1 of 1

APPLICATION NO. : 10/644503 DATED : March 29, 2005

INVENTOR(S) : Steven L. Naberhuis et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page, item (56), under "FOREIGN PATENT DOCUMENTS", in column 2, line 4, delete "EP" and insert -- WO --, therefor.

In column 5, line 2, delete "10" and insert -- 110 --, therefor.

In column 5, line 15, delete "a "I" bit," and insert -- a "1" bit, --, therefor.

In column 5, line 51, delete "FIG. 1" and insert -- FIG. 5 --, therefor.

In column 9, line 57, after "understood" insert -- by those skilled in --.

In column 9, line 58, after "without" insert -- departing from the --.

In column 10, line 37, in Claim 14, after "device" insert --, --.

In column 10, line 52, in Claim 17, delete "(MM)" and insert -- (MIM) --, therefor.

In column 11, line 15, in Claim 24, delete "n++semiconductor" and insert -- n++ semiconductor --, therefor.

Signed and Sealed this

Tenth Day of November, 2009

Javid J. Kappos

David J. Kappos

Director of the United States Patent and Trademark Office